BOARD OF TECHNICAL EDUCATION, DELHI

No F.53(0)/2025-Exam./BTE   Dated :06/05/25

Electronic Engineering(Digital Electronics & Microprocessor System Design)

Performance of the candidates (Compartment) of V semester of three years
Diploma Course in Engg. (in above mentioned branch)pertaining to examina-
-tion held in JAN 2025 is communicated as under.The marks of candidates
who have cleared full subjects out of an aggregate of 1400 are indicated
against their roll numbers.

Roll No	Subjects in which declared passed / Marks Obtained
2021071004	ALL / 802
2021071007	ALL / 758
2021071009	ALL / 823
2021071012	ED570, ED571, ED572, ED573, ED580, ED581, ED582, ED583, ED584, ED585.
2021071020	ALL / 819
2021071031	ALL / 739

Checked by   Rechecked by

NOTE :
1. Eligible candidates who have not been successful in any of the subject or have not appeared in any subject are notified as failed

2. The performance of the candidates who have been placed under P.L.O. will be communicated separately, if necessary.

SUBJECT CODES :

Theories
ED570   Communication System
ED571   Digital System Design
ED572   Troubleshooting and Maint. of Elec. Equ.
ED573   Microprocessor Based System Design
ED574   Object Oriented Prog. using C++

Practicals
ED580   Communication System
ED581   Digital System Design
ED582   Troubleshooting and Maint. of Elec. Equ.
ED583   Microprocessor Based System Design
ED584   Object Oriendted Prog. using C++
ED585   Industrial Training Report Presentation

Forwarded to the Principal, Chhotu Ram Rural Institute of Technology, Kanjhawla, Delhi for information and necessary action.

(S. P. MEHER)
CONTROLLER

(COMPUTER CENTRE, BTE)